

McKay, Information Technology Laboratory, Attention: Lightweight Cryptographic Algorithm Submissions, 100 Bureau Drive—Stop 8930, National Institute of Standards and Technology, Gaithersburg, MD 20899–8930. Submissions may also be sent by email to: [lightweight-crypto@nist.gov](mailto:lightweight-crypto@nist.gov). Note that for email submissions, some of the supporting documentation requires a signature and must be physically mailed to the above address. See <https://csrc.nist.gov/Projects/Lightweight-Cryptography> for complete submission instructions.

**FOR FURTHER INFORMATION CONTACT:** For general information, send email to [lightweight-crypto@nist.gov](mailto:lightweight-crypto@nist.gov). For questions related to a specific submission package, contact Dr. Kerry McKay, National Institute of Standards and Technology, 100 Bureau Drive, Mail Stop 8930, Gaithersburg, MD 20899–8930, email: [kerry.mckay@nist.gov](mailto:kerry.mckay@nist.gov), or by telephone: (301) 975–4969.

A public email list has been set up for announcements, as well as a forum to discuss the standardization effort being initiated by NIST. For directions on how to subscribe, please visit <https://csrc.nist.gov/Projects/Lightweight-Cryptography>.

**SUPPLEMENTARY INFORMATION:** The deployment of small computing devices, such as RFID tags, industrial controllers, sensor nodes and smart cards, is becoming increasingly common. The shift in focus from desktop computers to small devices brings a wide range of new security and privacy concerns. In many conventional cryptographic standards, the tradeoff between security, performance and resource requirements was optimized for desktop and server environments, and this makes the standards difficult or impossible to implement in resource-constrained devices. Therefore, when current NIST-approved algorithms can be engineered to fit within the limited resources of constrained environments, their performance may not be acceptable.

In recent years, there has been increased demand for cryptographic standards that are tailored for constrained devices. NIST has decided to create a portfolio of lightweight cryptographic algorithms, designed for limited use in applications and environments where cryptographic operations are performed by constrained devices that are unable to use existing NIST standards.

Previously, NIST solicited public comment on draft minimum acceptability requirements, submission requirements, and evaluation criteria for candidate algorithms (83 FR 22251, May

14, 2018). The comments received are posted at <https://csrc.nist.gov/Projects/Lightweight-Cryptography>, along with a summary of the changes made as a result of these comments.

The purpose of this notice is to announce that nominations for lightweight candidate algorithms may now be submitted, up until the final deadline of February 25, 2019. Complete instructions on how to submit a candidate package, including the minimal acceptability requirements, are posted at <https://csrc.nist.gov/Projects/Lightweight-Cryptography>.

**Authority:** 15 U.S.C. 278g–3.

**Kevin A. Kimball,**  
Chief of Staff.

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**BILLING CODE 3510–13–P**

## DEPARTMENT OF COMMERCE

### National Institute of Standards and Technology

#### National Institute of Standards and Technology Performance Review Board Membership

**AGENCY:** National Institute of Standards and Technology, Department of Commerce.

**ACTION:** Notice.

**SUMMARY:** This notice lists the membership of the National Institute of Standards and Technology Performance Review Board (NIST PRB) and supersedes the list published on September 15, 2017.

**DATES:** The changes to the NIST PRB membership list announced in this notice are effective August 27, 2018.

**FOR FURTHER INFORMATION CONTACT:** Didi Hanlein at the National Institute of Standards and Technology, (301) 975–3020 or by email at [desiree.hanlein@nist.gov](mailto:desiree.hanlein@nist.gov).

**SUPPLEMENTARY INFORMATION:** The National Institute of Standards and Technology Performance Review Board (NIST PRB or Board) reviews performance appraisals, agreements, and recommended actions pertaining to employees in the Senior Executive Service and ST–3104 employees. The Board makes recommendations to the appropriate appointing authority concerning such matters so as to ensure the fair and equitable treatment of these individuals.

This notice lists the membership of the NIST PRB and supersedes the list published in the **Federal Register** on September 15, 2017 (82 FR 43335).

NIST PRB Members:

Jennifer Ayers (C), Director, Office of the Secretary Financial Management, Department of Commerce, Washington, DC 20230, Appointment Expires: 12/31/18.

Joannie Chin (C) (alternate), Deputy Director, Engineering Laboratory, National Institute of Standards & Technology, Gaithersburg, MD 20899, Appointment Expires: 12/31/19.

Robert Fangmeyer (C) (alternate), Director, Baldrige Performance Excellence Program, National Institute of Standards & Technology, Gaithersburg, MD 20899, Appointment Expires: 12/31/18.

Howard Harary (C), Director, Engineering Laboratory, National Institute of Standards & Technology, Gaithersburg, MD 20899, Appointment Expires: 12/31/18.

James St. Pierre (C) (alternate), Deputy Director, Information Technology Laboratory, National Institute of Standards & Technology, Gaithersburg, MD 20899, Appointment Expires: 12/31/18.

Carroll Thomas (C), Director, Hollings Manufacturing Extension Partnership Program, National Institute of Standards & Technology, Gaithersburg, MD 20899, Appointment Expires: 12/31/19.

Alexander Zemek (NC), Chief of Staff, Bureau of Industry and Security, Washington, DC 20230, Appointment Expires: 12/31/19.

**Authority:** 5 U.S.C. 4301 *et seq.*

**Kevin A. Kimball,**  
Chief of Staff.

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## DEPARTMENT OF COMMERCE

### National Institute of Standards and Technology

#### Meeting of the Visiting Committee on Advanced Technology

**AGENCY:** National Institute of Standards and Technology, Department of Commerce.

**ACTION:** Notice of public meeting.

**SUMMARY:** National Institute of Standards and Technology (NIST)'s Visiting Committee on Advanced Technology (VCAT or Committee) will meet on Tuesday, October 16, 2018, from 8:30 a.m. to 5:00 p.m. Mountain Time, and Wednesday, October 17, 2018, from 8:30 a.m. to 11:30 a.m. Mountain Time. The VCAT is composed of not fewer than 9 members appointed by the NIST Director, eminent in such fields as business, research, new